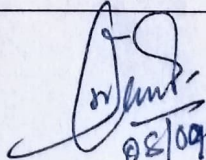


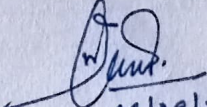
Discipline: ELECTRICAL	Semester: 5TH	Name of The Teaching Faculty: ER.SOUMYA SHYAMALI MAHAPATRA
Subject: DEC&MP	NO OF CLASSES/WEEKES 05	SEMESTER FROM: 15.9.2022-22.12.2022 NO OF WEEKS : 15
WEEKS	CLASS/DAY	THEORY/PRACTICAL TOPICS
15.9.2022 TO 17.9.2022	1 <sup>ST</sup>	. BASICS OF DIGITAL ELECTRONICS 1.1 Binary, Octal, Hexadecimal number systems and compare with Decimal system. Binary addition, subtraction, Multiplication and Division
	2 <sup>ND</sup>	<b>BISWAKRMA PUJA</b>
19.9.2022 TO 24.9.2022	1 <sup>ST</sup>	1's complement and 2's complement numbers for a binary number
	2 <sup>ND</sup>	Subtraction of binary numbers in 2's complement method
	3 <sup>RD</sup>	Use of weighted and Un-weighted codes & write Binary equivalent number for a number in 8421, Excess-3 and Gray Code and vice-versa
	4 <sup>TH</sup>	Use of weighted and Un-weighted codes & write Binary equivalent number for a number in 8421, Excess-3 and Gray Code and vice-versa
	5 <sup>TH</sup>	Importance of parity Bit
26.9.2022 TO 1.10.2022	1 <sup>ST</sup>	Logic Gates: AND, OR, NOT, NAND, NOR and EX-OR gates with truth table
	2 <sup>ND</sup>	Realize AND, OR, NOT operations using NAND, NOR gates
	3 <sup>RD</sup>	Different postulates and De-Morgan's theorems in Boolean algebra.
	4 <sup>TH</sup>	Use Of Boolean Algebra For Simplification Of Logic Expression
	5 <sup>TH</sup>	
3.10.2022 TO 8.10.2022	1 <sup>ST</sup>	<b>VIJAYA DASAMI</b>
	2 <sup>ND</sup>	
	3 <sup>RD</sup>	
	4 <sup>TH</sup>	
	5 <sup>TH</sup>	
10.10.2022 TO 15.10.2022	1 <sup>ST</sup>	Karnaugh Map For 2,3,4 Variable, Simplification Of SOP And POS Logic Expression Using K-Map
	2 <sup>ND</sup>	Karnaugh Map For 2,3,4 Variable, Simplification Of SOP And POS Logic Expression Using K-Map
	3 <sup>RD</sup>	COMBINATIONAL LOGIC CIRCUITS 2.1 Give the concept of combinational logic circuits
	4 <sup>TH</sup>	Half adder circuit and verify its functionality using truth table
	5 <sup>TH</sup>	3 Realize a Half-adder using NAND gates only and NOR gates only
17.10.2022 TO 22.10.2022	1 <sup>ST</sup>	Full adder circuit and explain its operation with truth table.
	2 <sup>ND</sup>	Realize full-adder using two Half-adders and an OR – gate and write truth table
	3 <sup>RD</sup>	Full subtractor circuit and explain its operation with truth table
	4 <sup>TH</sup>	Operation of 4 X 1 Multiplexers and 1 X 4 demultiplexer
	5 <sup>TH</sup>	Working of Binary-Decimal Encoder & 3 X 8 Decoder
	1 <sup>ST</sup>	<b>DIWALI</b>

  
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29.10.2022	2 <sup>ND</sup>	Working of Two bit magnitude comparato
	3 <sup>RD</sup>	Give the idea of Sequential logic circuit
	4 <sup>TH</sup>	State the necessity of clock and give the concept of level clocking and edge triggering
	5 <sup>TH</sup>	Clocked SR flip flop with preset and clear inputs.
31.10.2022 TO	1 <sup>ST</sup>	Construct level clocked JK flip flop using S-R flip-flop and explain with truth table
	2 <sup>ND</sup>	Construct level clocked JK flip flop using S-R flip-flop and explain with truth table
5.11.2022	3 <sup>RD</sup>	Concept of race around condition and study of master slave JK flip flop.
	4 <sup>TH</sup>	Concept of race around condition and study of master slave JK flip flop.
	5 <sup>TH</sup>	Give the truth tables of edge triggered D and T flip flops and draw their symbols. 3.8 Applications of flip flop
7.11.2022 TO	1 <sup>ST</sup>	LAST MONDAY OF KARTIKA
	2 <sup>ND</sup>	RAHAS PURNIMA
	3 <sup>RD</sup>	Give the truth tables of edge triggered D and T flip flops and draw their symbols. 3.8 Applications of flip flop
12.11.2022	4 <sup>TH</sup>	Define modulus of a counter
	5 <sup>TH</sup>	4-bit asynchronous counter and its timing diagram.
14.11.2022 TO	1 <sup>ST</sup>	Asynchronous decade counter.
	2 <sup>ND</sup>	4-bit synchronous counter
	3 <sup>RD</sup>	PRATHAMASTAMI
19.11.2022	4 <sup>TH</sup>	Distinguish between synchronous and asynchronous counters
	5 <sup>TH</sup>	State the need for a Register and list the four types of registers.
21.11.2022 TO	1 <sup>ST</sup>	Working of SISO, SIPO, PISO, PIPO Register with truth table using flip flop.
	2 <sup>ND</sup>	Working of SISO, SIPO, PISO, PIPO Register with truth table using flip flop.
26.11.2022	3 <sup>RD</sup>	8085 MICROPROCESSOR Introduction
	4 <sup>TH</sup>	Introduction to Microprocessors, Microcomputers
	5 <sup>TH</sup>	Architecture of Intel 8085A Microprocessor and description of each block.
28.11.2022 TO	1 <sup>ST</sup>	Architecture of Intel 8085A Microprocessor and description of each block.
	2 <sup>ND</sup>	Pin diagram and description.
	3 <sup>RD</sup>	Pin diagram and description.
3.12.2022	4 <sup>TH</sup>	Stack, Stack pointer & stack top
	5 <sup>TH</sup>	Stack, Stack pointer & stack top
5.12.2022 TO	1 <sup>ST</sup>	Interrupts Opcode & Operand,
	2 <sup>ND</sup>	Differentiate between one byte, two byte & three byte instruction with example.
10.12.2022	3 <sup>RD</sup>	Instruction set of 8085 example9 Addressing mod
	4 <sup>TH</sup>	LAST THURSDAY OF MARGASIRA
	5 <sup>TH</sup>	Fetch Cycle, Machine Cycle, Instruction Cycle, T-State

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12.12.2022 TO	1 <sup>ST</sup>	Timing Diagram for memory read, memory write, I/O read, I/O write
	2 <sup>ND</sup>	Timing Diagram for 8085 instruction
	3 <sup>RD</sup>	Counter and time delay. Simple assembly language programming of 8085.
17.12.2022	4 <sup>TH</sup>	INTERFACING AND SUPPORT CHIPS 5.1 Basic Interfacing Concepts, Memory mapping & I/O mapping
	5 <sup>TH</sup>	Functional block diagram and description of each block of Programmable peripheral interface Intel 8255
19.12.2022 TO	1 <sup>ST</sup>	Functional block diagram and description of each block of Programmable peripheral interface Intel 8255
	2 <sup>ND</sup>	Application using 8255: Seven segment LED display, Square wave generator, Traffic light Controller
24.12.2022	3 <sup>RD</sup>	Application using 8255: Seven segment LED display, Square wave generator, Traffic light Controller
	4 <sup>TH</sup>	<b>Attendance close</b>
	5 <sup>TH</sup>	

  
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